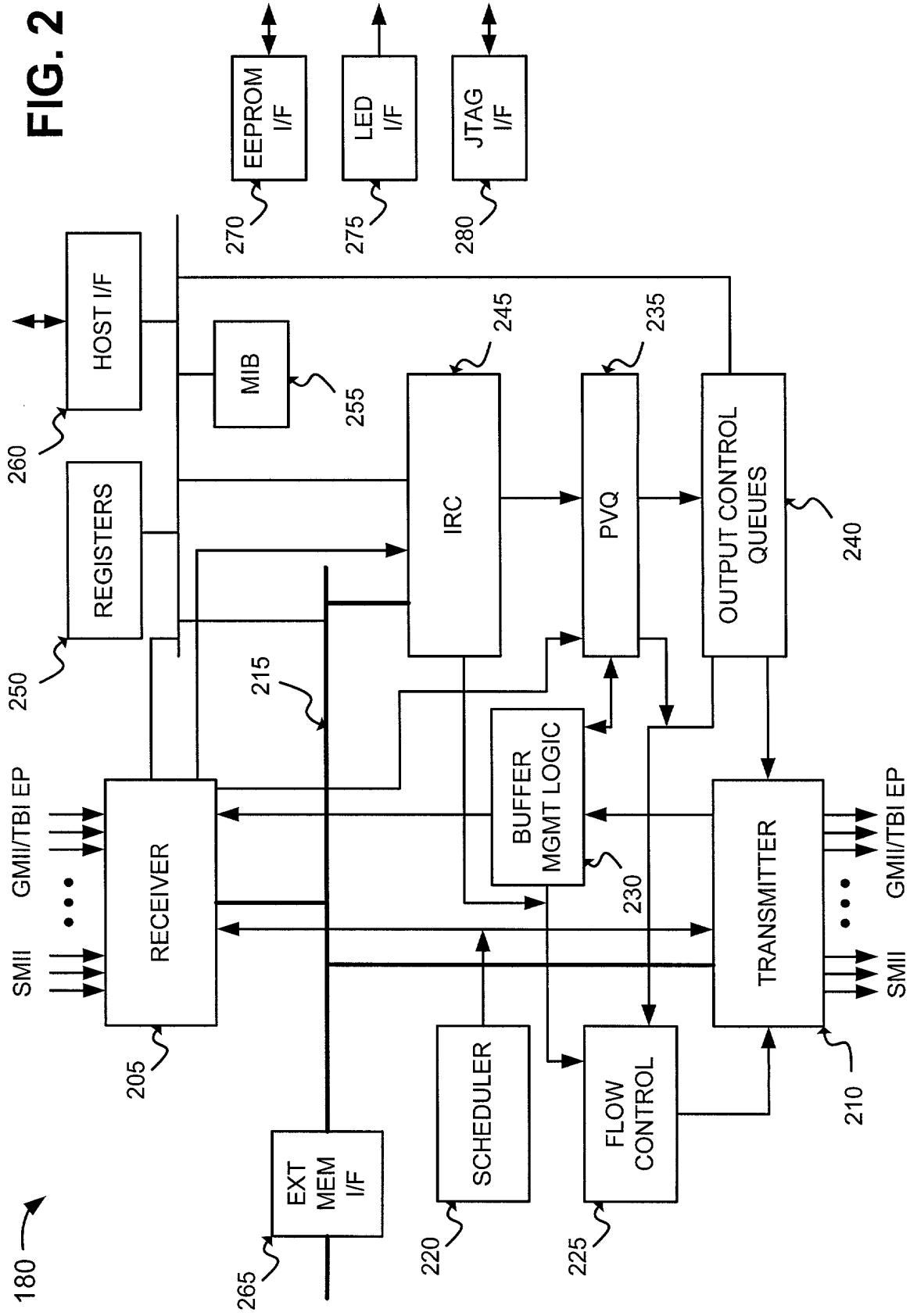




FIG. 2





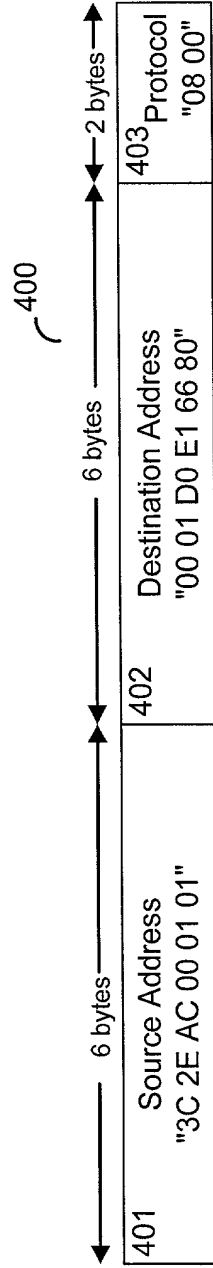


Fig. 4

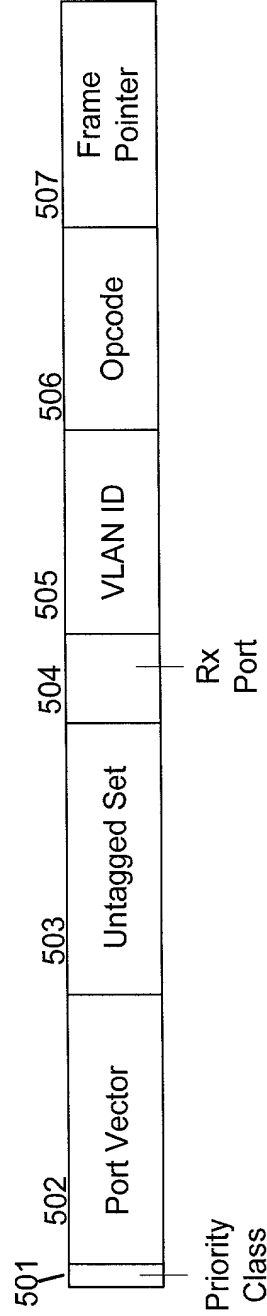


Fig. 5

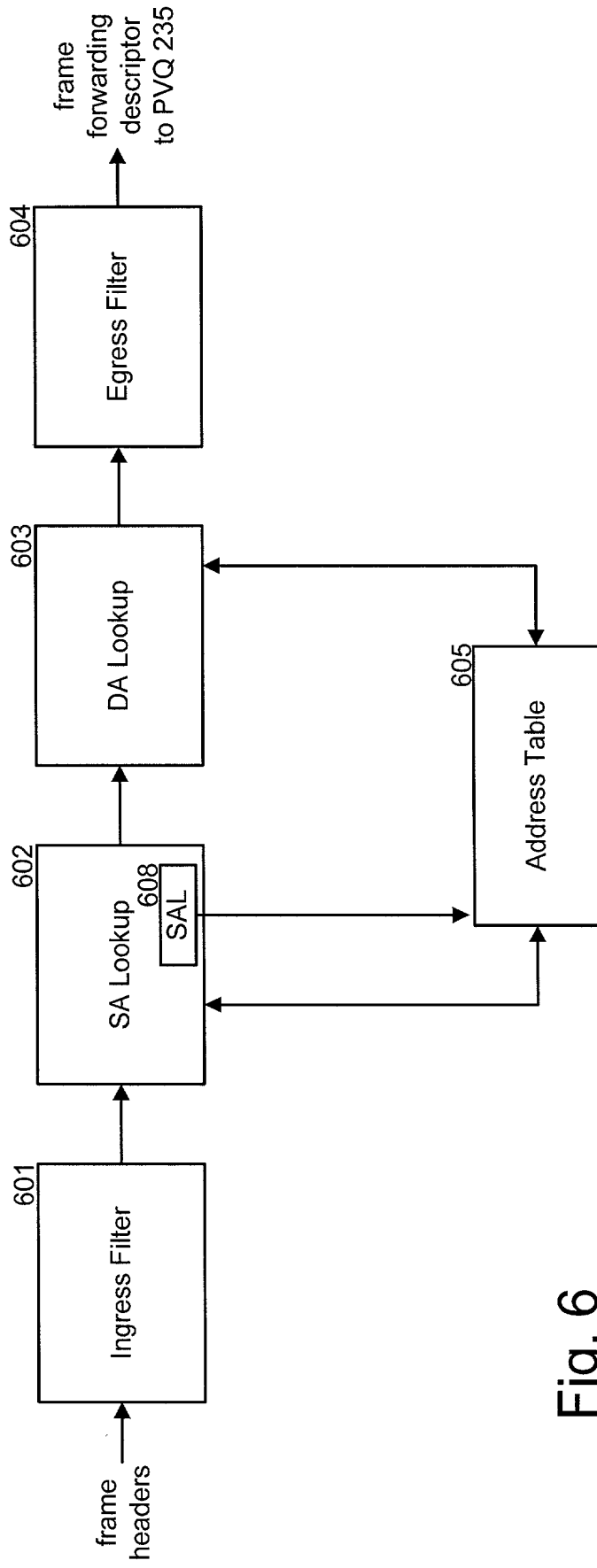


Fig. 6

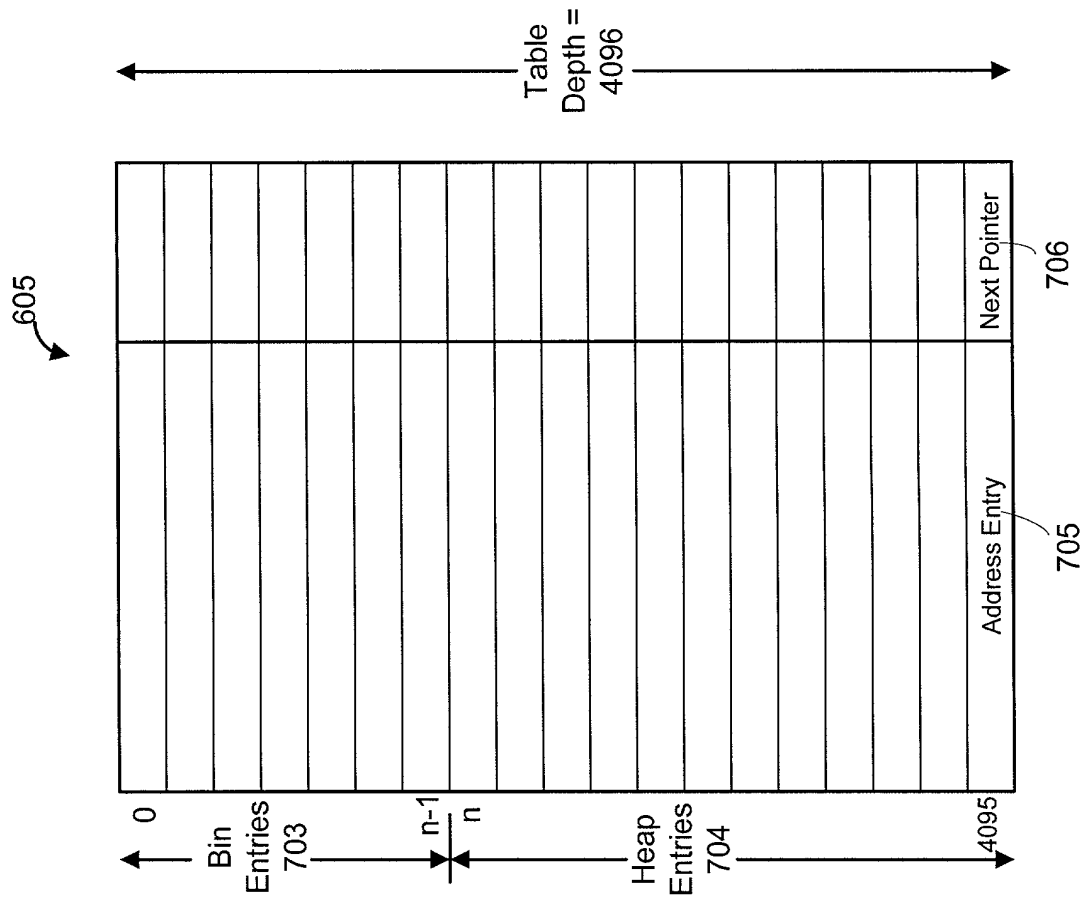


Fig. 7

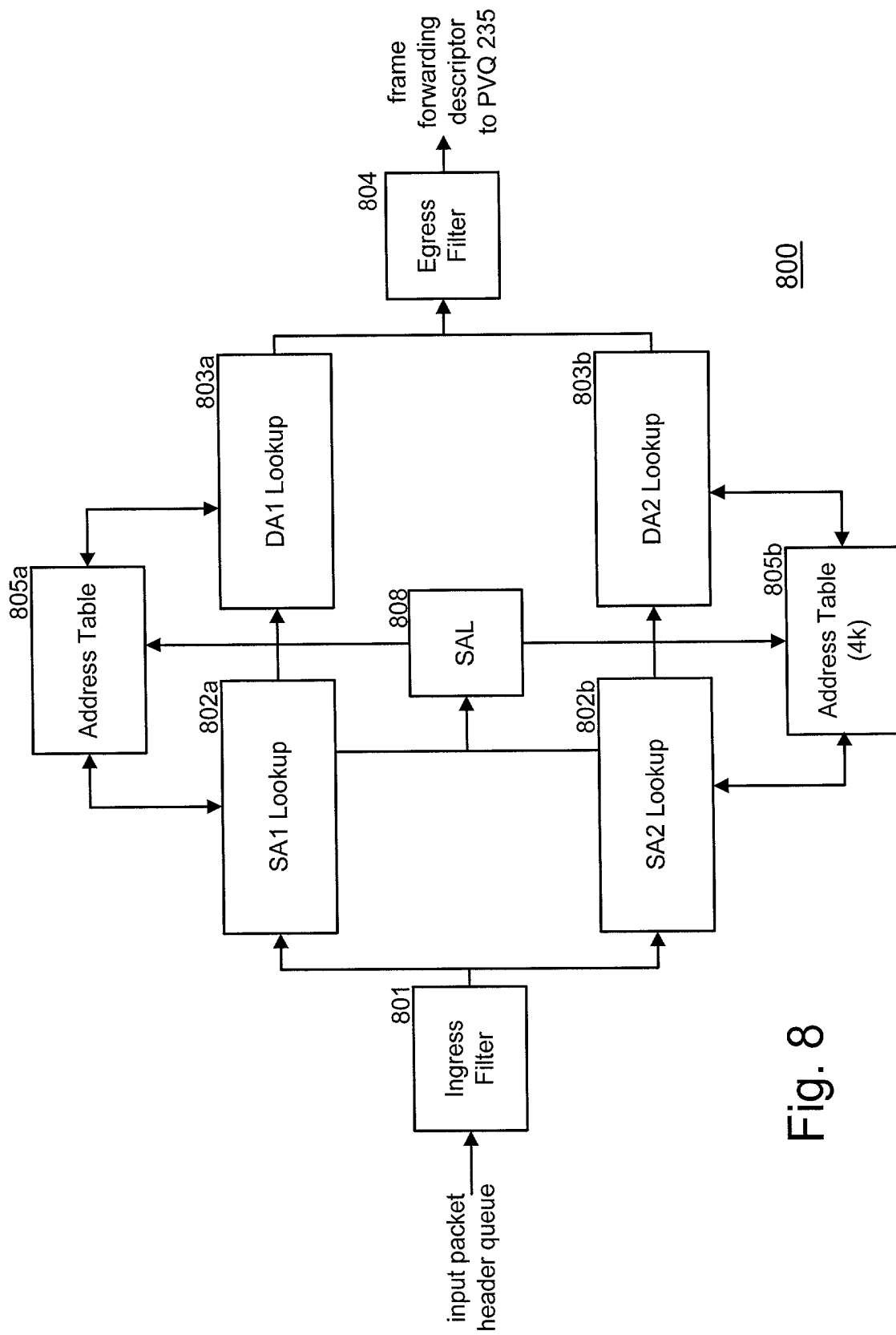


Fig. 8

Fig. 9 is a block diagram of a network device 900. The device 900 includes an input packet header queue, an ingress filter 901, two source address lookups (SA1 Lookup 902a and SA2 Lookup 902b), two destination address lookups (DA1 Lookup 903a and DA2 Lookup 903b), an address table 910, and an egress filter 904. The input packet header queue feeds into the ingress filter 901. The ingress filter 901 feeds into both SA1 Lookup 902a and SA2 Lookup 902b. SA1 Lookup 902a includes a source address lookup (SAL) block 908a. SA2 Lookup 902b includes a source address lookup (SAL) block 908b. Both SA1 Lookup 902a and SA2 Lookup 902b feed into the address table 910. The address table 910 feeds into both DA1 Lookup 903a and DA2 Lookup 903b. DA1 Lookup 903a feeds into the egress filter 904. DA2 Lookup 903b feeds into the egress filter 904. The egress filter 904 outputs a frame forwarding descriptor to PVQ 235.

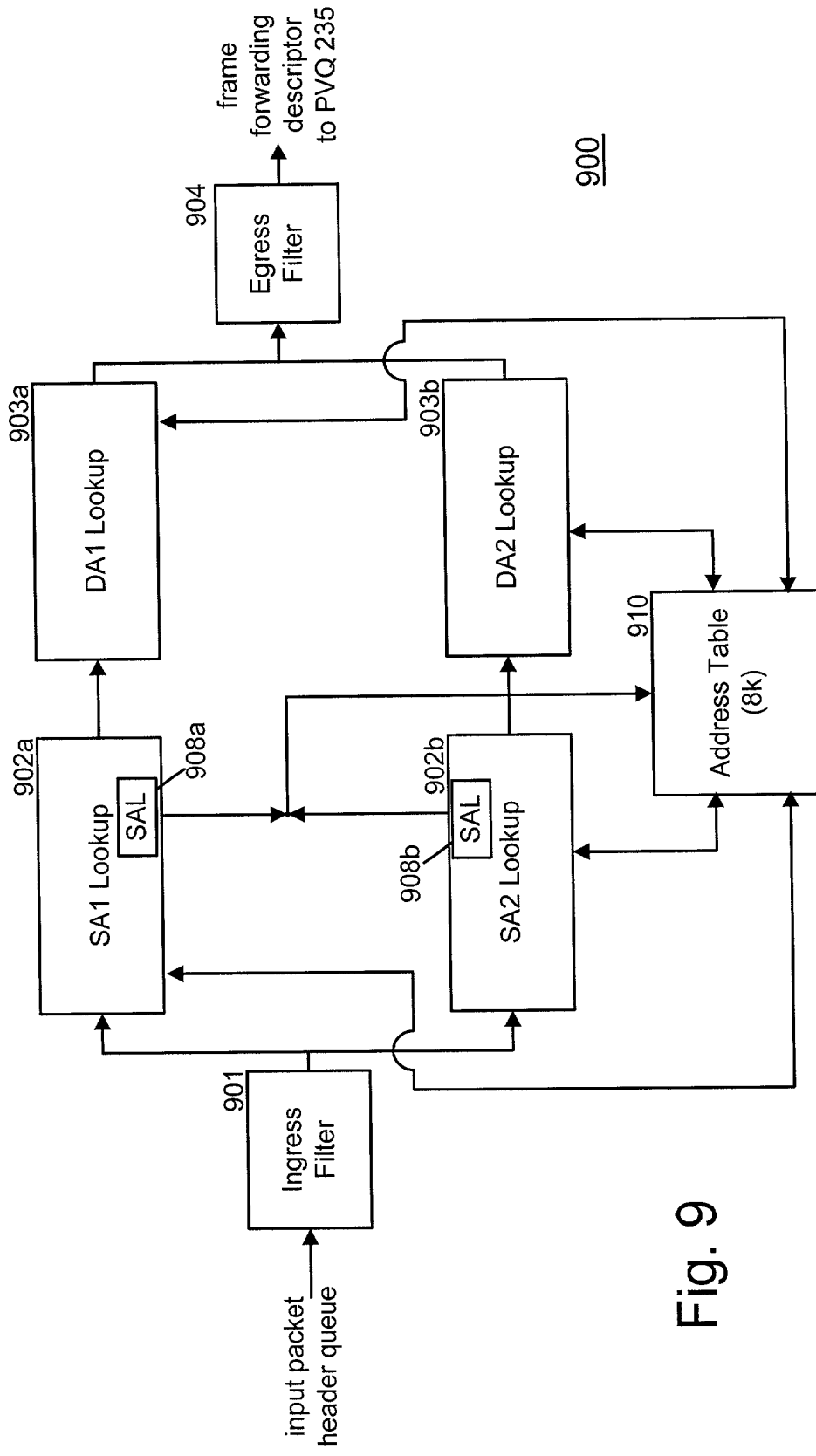


Fig. 9